

Practical Safety Integrity Level Determination and Verification Training Course

10 – 14 May 2027
Kuala Lumpur

AGILE LEADERS
Training Center

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Overview

The Practical Safety Integrity Level Determination and Verification Training Course is a one-week course for instrumentation engineers, process safety specialists, and control systems professionals who leave with a SIL assessment and loop verification dossier to govern critical safety instrumented functions. Process facilities frequently struggle with subjective risk ranking, inaccurate failure rate assumptions, miscalculated proof-test intervals, and unverified architectural constraints under IEC 61511. This course resolves these challenges through calibrated risk graphs, quantitative Layer of Protection Analysis models, and rigorous Probability of Failure on Demand calculations. Agile Leaders Training Center delivers the Practical Safety Integrity Level Determination and Verification Training Course.

Who Should Attend

- Process safety engineers responsible for hazard identification, risk reduction factor targets, and safety instrumented function definitions.
- Instrumentation and control engineers designing safety instrumented systems, interlock architectures, and loop logic solvers.
- Operations integrity leads managing bypass permits, safety override logs, and operational risk assessments.
- Maintenance reliability specialists scheduling proof tests, analyzing equipment failure rates, and maintaining field device diagnostics.
- Capital project managers overseeing safety lifecycle deliverables, vendor reliability data packages, and safety requirement compliance.

The course assumes participants actively interpret piping and instrumentation diagrams, process hazard analyses, and instrument loop sheets, and it leaves out introductory sensor wiring and basic industrial electronics.

Departments and Industries

The course draws operational cases from process facilities and automated industrial production plants.

- Process safety and technical health, safety, and environment departments
- Control systems, instrumentation, and automation engineering divisions
- Plant maintenance, reliability, and asset integrity units
- Chemical manufacturing and industrial gas refining facilities
- Offshore hydrocarbons processing and midstream pipeline operations
- Power generation plants and heavy petrochemical manufacturing complexes

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Learning Objectives

By the end of this course, participants will be able to:

- Apply calibrated risk graphs and risk matrices to establish target safety integrity levels for safety instrumented functions.
- Analyze initiating event frequencies and independent protection layers using semi-quantitative Layer of Protection Analysis worksheets.
- Calculate average Probability of Failure on Demand values and Spurious Trip Rates across diverse voting sensor and valve architectures.
- Evaluate Hardware Fault Tolerance requirements and Route 1H and 2H architectural constraints in accordance with IEC 61511 and IEC 61508.
- Build standardized Safety Requirement Specifications documenting functional logic, safe states, and proof-test intervals.
- Diagnose systemic operational failures by evaluating maintenance bypass management and proof test diagnostic coverage metrics.

Course Agenda

Day 1: Hazard Identification and Target SIL Selection

- Safety lifecycle management principles and framework requirements under IEC 61511 and IEC 61508
- Hazard identification linkage translating HAZOP scenario records into Safety Instrumented Function definitions
- Calibrated Risk Graph calibration models and corporate tolerable risk targets
- Risk Matrix application protocols for personnel safety and environmental risk classification
- Safety integrity level allocation methods for continuous and demand mode safety functions

Day 2: Semi-Quantitative SIL Determination Using LOPA

- Layer of Protection Analysis methodology and initiating event frequency databases
- Independent Protection Layer qualification criteria covering specificity, independence, and dependability
- Enabling conditions, conditional modifiers, and ignition probability credit allocation
- Risk Reduction Factor derivation and target SIL assignment worksheets
- Mitigation layer evaluation integrating basic process control systems and physical relief devices

Day 3: Safety Requirement Specifications and System Architecture

- Safety Requirement Specifications documentation and Cause and Effect logic matrix structures
- Hardware Fault Tolerance standards and architectural constraint compliance under Route 1H and Route 2H
- Failure Modes, Effects, and Diagnostic Analysis data interpretation and Safe Failure Fraction calculations
- Subsystem voting topologies analyzing 1oo1, 1oo2, 2oo2, and 2oo3 redundant configurations
- Common cause failure modeling using the Beta factor method for redundant instrument channels

Day 4: SIL Verification Mathematics and Lifecycle Operations

- Average Probability of Failure on Demand calculation formulas for simplex and voted architectures
- Spurious Trip Rate evaluation and economic reliability trade-off analysis
- Proof-test interval determination, proof-test coverage percentages, and partial stroke testing credit
- Maintenance override management, bypass administration logs, and operational risk governance
- Operational lifecycle monitoring, field failure tracking, and industrial OT cybersecurity alignment under IEC 62443

Day 5: SIL Verification Simulation and Capstone Execution

- Suggested activity: Model a multi-node Layer of Protection Analysis worksheet for a runaway reaction hazard
- Suggested activity: Calculate PFDavg and verify architectural constraints for a high-pressure separator shutdown loop
- Suggested activity: Audit a field Safety Requirement Specification document against vendor failure rate data packages
- Suggested activity: Formulate an optimized proof-test interval schedule balancing safety availability and spurious trips
- Capstone exercise: Assemble a complete SIL assessment and loop verification dossier for an operational facility

Practical Exercises

The course uses engineering calculation sheets, authentic industrial equipment failure datasets, and real-world process safety documentation.

- Suggested activity: Execute a Layer of Protection Analysis worksheet to evaluate protective credit and assign safety integrity levels.
- Suggested activity: Calculate Probability of Failure on Demand and Spurious Trip Rates for complex voted transmitter topologies.
- Suggested activity: Formulate a Safety Requirement Specifications document establishing proof-test procedures, voting logic, and safe state actions.
- Suggested activity: Construct an integrated SIL assessment and loop verification dossier synthesizing architectural checks and quantitative reliability models.

FAQs

Who suits the Practical Safety Integrity Level Determination and Verification Training Course?

The Practical Safety Integrity Level Determination and Verification Training Course suits instrumentation engineers, safety specialists, and automation professionals. The course assumes participants read piping and instrumentation diagrams and hazard analyses, leaving out basic electrical wiring fundamentals.

How does this course differ from general process hazard analysis courses?

General hazard analysis courses identify plant hazards using qualitative techniques like HAZOP. The Practical Safety Integrity Level Determination and Verification Training Course concentrates on quantitative risk reduction, mathematical SIL verification, hardware fault tolerance constraints, and safety lifecycle compliance under IEC 61511.

How is SIL determination conducted in the Practical Safety Integrity Level Determination and Verification Training Course?

The course applies calibrated risk graphs, corporate risk matrices, and semi-quantitative Layer of Protection Analysis. Participants quantify initiating event frequencies, validate independent protection layers, and calculate required Risk Reduction Factors.

How does the Practical Safety Integrity Level Determination and Verification Training Course verify safety loops?

Verification is performed using quantitative formulas for average Probability of Failure on Demand, Safe Failure Fraction, and Spurious Trip Rate. Participants assess voting architectures, common cause failures, and proof-test intervals against IEC 61511 Route 1H and 2H constraints.

Conclusion

Participants return to their organizations equipped with an actionable SIL assessment and loop verification dossier to govern facility safety instrumented functions. This work product validates safety integrity requirements, eliminates under-designed or over-designed shutdown loops, and substantiates regulatory compliance. Operating sites immediately experience enhanced asset integrity, reduced spurious trips, and verifiable functional safety risk reduction.